

# Performance of DCSK System with Blanking Circuit for Power-Line Communications

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**Abstract**—Providing from the robustness of differential chaos shift keying (DCSK) against linear and non-linear channel distortions, in this paper, we propose using this non-coherent modulation with blanking device as a potential candidate for implementation in power-line communications. The performance of this system is analyzed in the general case where background, impulsive and phase noises exist. We derive the probability of blanking error as well as the bit error rate with and without blanking. Moreover, the proposed system is compared to differential phase shift keying (DPSK) modulation to highlight the robustness of DCSK scheme against phase noise imperfection.

## I. INTRODUCTION

Power line communication (PLC) has been long used to transmit data over available power lines. The passion and enthusiasm into this field has lately been restored as PLC emerged as a capable rival in the broadband communication market for indoor in-building networking and also for the last-mile access [1]. Some features like plug-and-play connection, always-on access, higher data rate support as well as the application to remote and rural areas are only few out of many advantages that PLC can offer [2]. Standardization groups such as IEEE P.1901 and ETSI PLT are currently working on PLC to come up with effective modulation schemes for the imminent commercialization of PLC [3]. As a result of all these efforts, quite a good number of modulation schemes have been recommended to transmit digital information over PLC channels. Unlike the case of low data rate applications where phase and frequency shift keying are sufficient, for high data rate applications, orthogonal frequency division multiplexing (OFDM) as well as code division multiple access (CDMA) have been standing out as potential solutions for adoption into future PLC architectures [4]. The data transmission on electrical lines is not as easy as it seems. Indeed, the existence of unpredictable factors, whose power spectral density (PSD) span through wide bands of spectrum, can easily render communication over power lines impossible. Some of these factors that are mostly of noise/interference nature stemming from different sources and exhibiting different behaviours are stationary, background noise [5] and time-variant, impulsive noise [5]. The latter is the most threatening degradation with short

duration and random occurrence nature associated with a high power spectral density. The blanking method that is introduced in this study can nullify the effects of impulsive noise [6] to a large extent. Various chaos-based communication systems with both coherent and non-coherent detection have been proposed in the last decade [7]–[10]. This is due to the fact that, chaotic signals have instinctive wideband characteristics that make them a very well fit for various spread spectrum modulation schemes. In fact, similar to other spread spectrum modulation techniques, chaos-based modulations have advantages such as reduction of fading effects and jamming resistance [8], [9]. Differential chaos shift keying (DCSK) is the mostly promising scheme among chaos-based communication schemes [7]. The fundamental reason for such widespread adoption is that DCSK require neither chaotic synchronization nor knowledge of the channel state information (CSI) at the receiver [7], [11]. By the same token, the DCSK is more immune to multipath fading and to nonlinear distortions, when compared to the DPSK scheme, and is much more suitable for ultra wide band (UWB) applications [7], [8], [11].

In this paper, we investigate the application of DCSK modulation using blanking device at the receiver for power line communication. To the best of our knowledge, there is no previous study analysing such class of modulation in PLC systems. It is important to note that the DCSK system is chosen in this paper as a benchmark for *transmit reference* non-coherent chaos-based communication systems. In detailing our contrition in this paper, in Section II the DCSK modulation scheme and blanking device is proposed. In section III, the analytical expression of probability of blanking error is derived as well as the bit error rate probability expressions of the system in the presence of high impulsive noise power, phase noise and with and without the blanking device. Moreover, we discuss the threshold level on the performance of the system and we validate our analytical expressions by computer simulations. In section IV, the performance of DPSK and DCSK is compared through simulation in order to show the resistance of the latter to phase noise interference.

## II. DCSK SYSTEM WITH BLANKING DEVICE

Fig.1 is a diagrammatic representation of the general structure of DCSK communication system where chaotic samples

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spread a given information bit  $b_i$ . At the output of the DCSK modulator, each bit  $b_i = \pm 1$  is composed of two equal portions, each with length equal of  $\beta$ ; the first is the *reference* portion which only consists of the chaotic sequence samples. The second is the *data-carrying* portion which consists of the multiplication of chaotic sequences with the incoming bit stream  $b_i$ . If  $b_i = +1$  is to be transmitted, the data-bearing sequence is exactly the reference sequence, and if  $b_i = -1$  is to be sent, the data-carrying sequence is an inverted version of the reference sequence. The spreading factor in DCSK systems is defined as the number of chaotic samples used to spread each bit and is presented by  $2\beta$ , where  $\beta$  is an integer. Moreover,  $T_{DCSK} = 2T_b = 2\beta T_c$  is the DCSK frame time interval for each bit and  $T_c$  is the chip interval. During the  $i^{\text{th}}$  bit time interval, the discrete form of the discrete baseband signal sequence at the output of the transmitter  $s_{k,i}$  can be given by

$$s_{k,i} = \begin{cases} x_{k,i}, & 0 < k \leq \beta, \\ b_i x_{k-\beta,i}, & \beta < k \leq 2\beta, \end{cases} \quad (1)$$

where  $x_{k,i}$  is the chaotic sequence used as the reference signal and  $x_{k-\beta,i}$  is its delayed version. After travelling through the PL channel, the received signal  $r_{k,i}$  is corrupted with impulsive and background noise [5]. Hence, the total noise can be modelled as

$$n_{k,i} = w_{k,i} + I_{k,i}, \quad (2)$$

where  $n_{k,i}$  is the total noise,  $w_{k,i}$  is the complex additive white Gaussian noise (AWGN) and  $I_{k,i}$  is the impulsive complex noise affecting  $i^{\text{th}}$  bit at time  $k$ . It is important to note that the different signal components present in equation (2) are mutually independent. In this paper, we consider the two-mixture Gaussian model [12] in our analysis.

$$I_{k,i} = B_{k,i} g_{k,i}, \quad (3)$$

where  $g_{k,i}$  is a zero mean white Gaussian noise and  $B_{k,i}$  is the Bernoulli process with probability  $P(B_{k,i} = 1) = p$ , where  $p$  represents the probability of occurrence of the impulsive noise. Hence, the total probability density function (PDF) of the sum of the noise signals  $w_{k,i}$  and  $I_{k,i}$  in (2) can be expressed as  $f(n_k) = (1-p)\mathcal{N}(0, \sigma_w^2) + p\mathcal{N}(0, \sigma_w^2 + \sigma_I^2)$ . Here,  $\mathcal{N}(\cdot)$  is the PDF of the Gaussian distribution. Also,  $\sigma_w^2$  is the background AWGN noise variance and  $\sigma_I^2$  is the impulsive noise variance. We denote by signal-to-noise ratio (SNR) the ratio of the power of the signal  $s_k$ , i.e.  $\sigma_s^2$ , to the power of the AWGN noise  $w_k$  represented by,  $\text{SNR} = 10 \log_{10} \sigma_s^2 / \sigma_w^2$ . On the other hand, the signal-to-impulsive noise ratio (SINR) is given as  $\text{SINR} = 10 \log_{10} \sigma_s^2 / \sigma_I^2$ . Whether the impulsive noise is present or not, at the receiver, the received signal  $r_{k,i}$  of  $i^{\text{th}}$  transmitted bit at time  $k$  may be expressed as

$$r_{k,i} = \begin{cases} s_{k,i} e^{j\phi_i} + w_{k,i} & \mathcal{H}_0 \\ s_{k,i} e^{j\phi_i} + w_{k,i} + I_{k,i} & \mathcal{H}_1 \end{cases}, \quad (4)$$

where  $\mathcal{H}_0$  and  $\mathcal{H}_1$  represent the hypotheses of the absence and the presence of impulsive noise having the following probabilities  $P(\mathcal{H}_0) = (1-p)$  and  $P(\mathcal{H}_1) = p$  respectively.

The exponent  $\phi_i \in [-\pi, \pi]$  in (4) is a uniformly distributed random variable representing the phase noise. It is important to note that this phase variable is maintained constant over more than one DCSK bit duration  $T_{DCSK}$ . As shown in Fig. 1, the received signal is first fed into the blanking device to compare its magnitude to a given blanking threshold  $T$ . The vector signal  $\mathbf{y}_i$  of  $2\beta$  samples exiting the blanking process can then be expressed as

$$\mathbf{y}_i = \begin{cases} \mathbf{r}_i, & E[||\mathbf{r}_i||] \leq T \\ 0, & E[||\mathbf{r}_i||] > T \end{cases} \quad (5)$$

where  $E[||\mathbf{r}_i||]$  is the average of the absolute values of  $2\beta$  samples corresponding to the  $i^{\text{th}}$  received DCSK bit. Since the blanking device is in fact a magnitude-based comparison filter, the terms  $\text{ang}(\cdot)$  and  $\text{exp}(j)$  are utilized to guard and retain the information content of the *phase* of the received signal, as elaborated in Fig. 1. It should be acknowledged that the blanking threshold must be optimized to assure the best outcome because a low value of  $T$  will wash out a large portion of the useful signal. Likewise, choosing  $T$  too large will let in most of the impulsive noise which noticeably degrades the BER performance. In order to demodulate the transmitted bits, the signal  $y_k$  at the output of the *blanking device* is correlated with its delayed complex conjugate version  $y_{k+\beta}^*$  and summed over the duration  $\beta T_c$ . The received bits are then estimated by computing the sign of the real part of the correlator output. The decision variable  $D_i$  of the  $i^{\text{th}}$  bit at the output of the correlator when  $I_k$  is absent becomes

$$D_i = \Re \left( \sum_{k=1}^{\beta} (x_{k,i} e^{j\phi_i} + w_{k,i}) (b_i x_{k,i} e^{j\phi_i} + w_{k+\beta,i})^* \right), \quad (6)$$

where  $\Re$  represents the real operator. This decision variable may be further expanded as

$$D_i = \sum_{k=1}^{\beta} b_i x_{k,i}^2 + \Re \left( \sum_{k=1}^{\beta} x_{k,i} (w_{k+\beta,i}^* e^{j\phi_i} + b_i w_{k,i} e^{-j\phi_i}) \right) + \Re \sum_{k=1}^{\beta} (w_{k+\beta,i}^* w_{k,i}). \quad (7)$$

Due to its easiness and good performance, the second-order Chebyshev polynomial function (CPF)  $x_{k+1} = 1 - 2x_k^2$  is used to generate chaotic sequences. The variance of the normalized chaotic map with zero mean is equal to one, i.e.  $V[x] = E[x^2] = 1$ , where  $E[\cdot]$  denotes the probabilistic expectation operator. As is it implied from (7), the useful signal  $b_i x_{k,i}^2$  in the decision variable is independent from the phase noise. This independence is due to the correlation between the received signal and its delayed complex conjugate replica.

### III. PERFORMANCE ANALYSIS OF DCSK SYSTEM OVER PLC CHANNEL

#### A. Blanking: False-alarm and mis-detection probabilities

In this section we derive the probability of blanking error  $P_b$  event which occurs when the magnitude of the received signal  $A_r = E[||\mathbf{r}_i||]$  exceeds the blanking threshold  $T$  in the

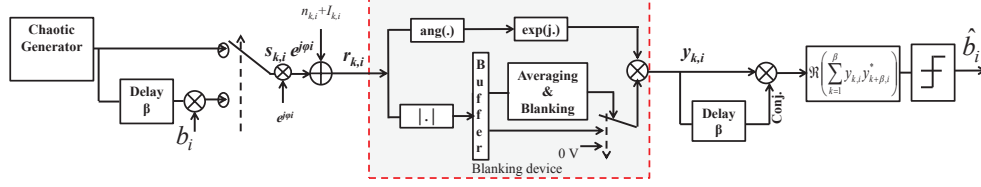


Fig. 1: Block diagram of the general structure of the DCSK communication system; (a) transmitter, receiver with blanking device.

absence of impulsive noise. This probability can be formulated as

$$P_b = Pr(A_r > T | \mathcal{H}_0) P(\mathcal{H}_0). \quad (8)$$

This expression leads to the probability of blanking error given by

$$P_b = (1 - p) \int_T^\infty f_{A_r}(r | \mathcal{H}_0) dr, \quad (9)$$

where  $f_{A_r}(r | \mathcal{H}_0)$  is the PDF of the amplitude of the DCSK signal in the absence of impulsive noise. Additionally, the bit energy of the DCSK is not constant and varies from one bit to another [8] which makes the analytical expression of the PDF not available in closed-form. Instead, the solution of (9) can be derived numerically. In a similar manner, the probability of miss detection can be computed as

$$P_m = p \int_{-\infty}^T f_{A_r}(r | \mathcal{H}_1) dr, \quad (10)$$

where  $f_{A_r}(r | \mathcal{H}_1)$  is the PDF of the amplitude of the DCSK signal in presence of impulsive noise. In the same vein, the expression  $P_m$  can only be derived numerically.

#### B. Bit error rate probability

From the above analysis, an error in the bit detection occurs in one of two cases; when the blanking device correctly predicts the absence of impulsive noise in the signal but the DCSK receiver makes an error in demodulating the bit; or when the system mis-detect the presence of the impulsive noise in the received signal and decodes the bits erroneously. Concluding these two cases, the total bit error rate with blanking device can be formulated as follows

$$\text{BER}_{\text{Blank}} = (1 - P_m) \text{BER}_{\text{BN}} + P_m \text{BER}_{\text{IBN}}, \quad (11)$$

where  $\text{BER}_{\text{Blank}}$ ,  $\text{BER}_{\text{BN}}$  and  $\text{BER}_{\text{IBN}}$  represent the total BER, the BER in the absence of impulsive noise and the BER in the presence of impulsive noise. For comparison, the total BER of the DCSK system without blanking is the sum the bit error rate in presence of background noise  $\text{BER}_{\text{BN}}$  multiplied but the probability of absence of the impulsive noise and the BER in presence of background and impulsive noise  $\text{BER}_{\text{IBN}}$  multiplied with the probability of occurrence  $p$  of this latter.

$$\text{BER}_{\text{PL}} = (1 - p) \text{BER}_{\text{BN}} + p \text{BER}_{\text{IBN}}, \quad (12)$$

where  $\text{BER}_{\text{PL}}$  is the total BER without the blanking device at the receiver side. When the impulsive noise is strong,

$\text{BER}_{\text{IBN}} \rightarrow 0.5$  in equations (11) and (12). To compute the BER expression in the absence of impulsive noise  $\text{BER}_{\text{BN}}$ , the mean and the variance of the decision variable given in (7) should be derived. For the  $i^{\text{th}}$  bit, the instantaneous mean of the decision variable is the mean of the useful signal given by

$$E[D_i] = \frac{b_i E_b}{2}, \quad (13)$$

where  $E_b = 2 \sum_{k=1}^{\beta} E[x_{k,i}^2]$  is the total energy of the transmitted DCSK bit. Moreover, because the the interference components are independent, as are the the noise signal and is delayed version, the conditional variance of the two remaining interference components present in the decision variable for the  $i^{\text{th}}$  bit can be expressed as

$$V[D_i] = \frac{E_b N_0 E[\cos(\phi_i)^2]}{2} + \beta \frac{N_0^2}{4}. \quad (14)$$

Knowing that  $E[\cos(\phi)^2] = 0.5$  and using (13) and (14) above, the average BER of the DCSK scheme in the presence of background and phase noises is derived as

$$\text{BER}_{\text{BN}} = \frac{1}{2} \int_0^\infty \text{erfc} \left( \frac{2N_0}{E_b} \left( 1 + \frac{\beta N_0}{E_b} \right)^{-0.5} \right) f(E_b) d(E_b). \quad (15)$$

where  $\text{erfc}(x)$  is the complementary error function defined as  $\text{erfc}(x) \equiv 2/\sqrt{\pi} \int_x^\infty e^{-u^2} du$  and  $f(E_b)$  is the PDF of the bit energy after spreading by chaotic signal.

#### IV. DISCUSSIONS AND SIMULATION RESULTS

In order to validate the performance of the DCSK scheme over power line channels, the analytical expressions for the probability of blanking error and BER are verified against simulation results. Fig. 2 shows the probability of blanking error of DCSK scheme for different SNR values versus the threshold  $T$  in dB. A careful examination of the performance curves shown in Fig. 2 reveals the extent to which simulation results perfectly validate the analytical expression provided in (9). For better performance, an adaptive threshold is required for such systems. In fact, even though lower blanking thresholds can reduce the probability of blanking error, but it increases re-transmissions of the bits nulled at the receiver side at the same time. The total bit error rate with blanking device is another important metric to be examined when analysing the performance. The obtained BER performance under power line communication channel for a spreading factor of  $\beta = 50$ ,  $T = 4.5$  and signal-to-impulsive noise ratio  $\text{SINR} = -2$

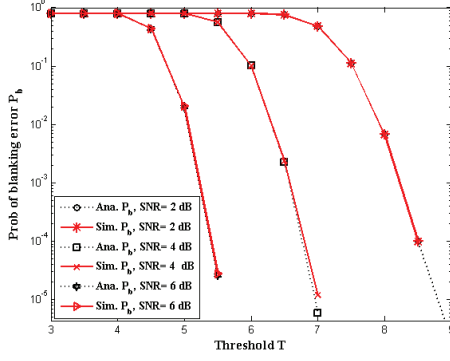


Fig. 2: Simulation, analytical probability of blanking error  $P_b$  with a spreading factor of  $\beta = 50$  and  $p = 0.2$ .

dB with and without a blanking device and in presence of phase noise is shown in Fig. 3. The phase noise is assumed constant during 4 time symbols for both systems. Likewise, simulation results warrant the accuracy of the BER expression given in equations (11) and (12). As observed in Fig. 3, the

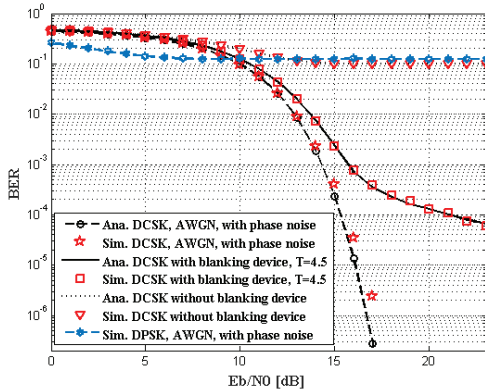


Fig. 3: Simulation, computed and BER for PLC channel with and without blanking device for a spreading factor of  $\beta = 50$  for  $T = 4.5$ ,  $\text{SNR} = -2$  dB, and  $p = 0.2$ .

proposed DCSK system with blanking device outperforms the conventional DCSK. In fact, this device eliminates the powerful impulsive noise by nulling the received signal. Naturally, when the impulsive noise affects many bits, the BER increases and the performance deteriorates. In addition, we observe a *flooring* in the total BER at high SNR. This comes from the miss-detected impulsive noise by the blanking circuit (lower flooring) or from the added impulsive noise when the system does not employ such a blanking circuit at all (upper flooring). Therefore, to preserve the quality of service of the transmission, it is better to re-transmit the affected bits/frames rather than decoding them with high probability of errors. This operation justifies the use of blanking device at the receiver side. Finally, we compared the BER performance of DCSK given in equation (15) and that of DPSK in the presence of phase noise and without the effect of the impulsive noise. As it is seen, the simulation again conforms to the analytical results.

Moreover, DCSK system exhibits a higher resistance to phase noise than DCSK. All these proves that our proposed scheme is a more suitable candidate for deployment in PLC scenarios.

## V. CONCLUSIONS

In this study, a blanking device with DCSK receiver was proposed for power-line communication. Our proposed scheme enhances the overall system performance in presence of impulsive and phase noise. Instead of decoding the corrupted bits with impulsive noise with a high probability of error, the blanking device nulls the received signal when this latter appears. Moreover, the performance of the DCSK is analysed. The probability of blanking error as well as the bit error rate expressions with and without the blanking device under power line communication channels are derived. Computer simulations conform to the derived analytical results proving that the proposed system outperforms the conventional DCSK when a blanking device is used. Moreover, DCSK system is compared to DPSK in presence of phase noise to show its resistance and superiority. Our findings proves that the DCSK system can be considered as a potential modulation scheme for power line communication systems. Future work will focus on optimizing the blanking threshold and analyzing the performance limits of the system.

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